

BSc Sem II Computer Science  
Assignment 4

ON COMPUTER ARCHITECTURE

- Q1) Illustrate Interconnection of Computer Units via System buses.
- Q2) Write all the steps for writing data in memory.
- Q3) Explain Cache memory by means of a diagram.
- Q4) Explain briefly different types of Cache memory mapping.
- Q5) Define nibble, words word size, 64-bit CPU, what is Hit Ratio.
- Q6) Explain with the help of a diagram that how data flow of a typical cache memory unit in computer system architecture.